



Indira Gandhi Delhi Technical University For Women
(Established by Govt. of Delhi vide Act 09 of 2012)
Kashmere Gate, Delhi-110006

F-AD-03

Department of Electronics & Communication Engineering
TIME TABLE

Even Sem. January –May 2021/ B. Tech 4th Semester

w.e.f: 8th January, 2021

Time	9-10 AM	10-11 AM	11-12 AM	12-1 PM	1-2 PM	2-3 PM	3-4 PM	4-5 PM
Day								
Mon	DM	OOPS	DSD	LIC		EMFT	DSD LAB (C)	
							LIC LAB (D)	
Tue		COA	DSD	OOPS		CS	EMFT	
Wed	COA	OOPS/ COA	LIC LAB (C)			CS	DSD LAB (D)	
			CS LAB (D)				EMFT LAB (C)	
Thu	OOPS LAB (C)		EMFT	DSD		LIC	CS LAB(C)	
							EMFT LAB (D)	
Fri	COA LAB		DM	CS		LIC	OOPS LAB (D)	

Sub. Code	Lab/ Course Name	Faculty Name
BEC-202	Linear Integrated Circuits	Dr. Richa Yadav
BEC 208	Communication Systems	Mr. Kanchan Sharma
BEC 204	Digital System Design	Dr. Shobha Sharma
BEC 206	EMFT	Ms. Neha Singh
BIT 204	OOPs /OOPS Lab	JRF/ Ms Nisha Rathee(C)/Mr. Gaurav Indra (D)
HMC 202	Disaster Management	Mr. Ashish Suman
BCS 202	COA (Elective) (classes combined with CSE1)	Ms Tanu (CSE Dept.)

Dr. Nidhi Goel

(HOD, ECE)

Pankaj

Dr. Pankaj Gupta
(Time Table In-charge, ECE Dept)



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TIME TABLE

Even Sem. January –May 2021/ B. Tech 6th Semester

w.e.f : 8th January, 2021

Time	9-10 AM	10-11 AM	11-12 AM	12-1 PM	1-2 PM	2-3 PM	3-4 PM	4-5 PM
Day								
Mon	EE	ME	ITC Lab (C)			DSP	VLSI	ITC
			ME Lab (D)					
Tue		ITC	VLSI	ME		DCCN	DCCN Lab (C)	
							DSP Lab (D)	
Wed	ME Lab (C)		ITC	DCCN		VLSI	ME	DSP
	ITC Lab (D)							
Thu	EE		VLSI	DSP		DCCN	VLSI Lab (C)	
							DCCN Lab (D)	
Fri	EE	ME	VLSI Lab (D)			ITC	DSP	DCCN
			DSP Lab (C)					

Sub. Code	Lab/ Course Name	Faculty Name
BEC 352	Microwave & Radar Engineering Lab (ME Lab)	Ms. Vidhi Bishnoi
BEC 354	Information Theory & Coding Lab (ITC Lab)	Ms. Neha Singh
BEC 356	VLSI Design Lab (VLSI Lab)	Dr. Vandana Niranjana
BIT 358	Data Communication & Networking Lab (DCCN Lab)	Ms. Greeshma Arya
BEC 360	Digital Signal Processing & its application Lab (DSP Lab)	Dr. Akash Tayal
BEC 302	Microwave & Radar Engineering (ME)	Ms. Vidhi Bishnoi
BEC 304	Information Theory & Coding (ITC)	Ms. Neha Singh
BEC 306	VLSI Design (VLSI)	Dr. Vandana Niranjana
BIT 308	Data Communication & Networking (DCCN)	Ms. Greeshma Arya
BEC 310	Digital Signal Processing and its applications (DSP)	Dr. Akash Tayal
BAS 312	Engineering Economics (EE)	Ms. Shivangi

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Even Sem. January –May 2021/ B. Tech 8th Semester

w.e.f: 8th January, 2021

Time	9-10 AM	10-11 AM	11-12 AM	12-1 PM	1-2 PM	2-3 PM	3-4 PM	4-5 PM
Day								
Mon	MAJOR PROJECT					MAJOR PROJECT		
Tue	MAJOR PROJECT		MC Lab (C)			OB	ESD	MC
			ESD Lab (D)					
Wed	MAJOR PROJECT		OB	ESD		WSN	WSN	MC
Thu	MAJOR PROJECT		MC	WSN		OB	ESD	
Fri	MAJOR PROJECT		WSN	ESD		MC	ESD Lab (C)	
							MC Lab (D)	

Sub. Code	Lab/ Course Name	Faculty Name
BEC 454	Mobile Communication Lab (MC Lab)	Mr. Kanchan Sharma
BCS 452	Embedded Systems Design Lab (ESD Lab)	Dr. Richa (C) and Ms Greeshma (D)
BAS 422	Organizational Behavior (Elective 2) (OB)	Prof. Ashwni Kumar
BEC 404	Mobile Communication (MC)	Mr. Kanchan Sharma
BCS 402	Embedded Systems Design (ESD)	Dr. Nidhi Goel
BCS 412	Wireless Sensor Networks (Elective 1) (WSN)	Ms. Greeshma Arya

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Even Sem. January –May 2021/ M. Tech (VLSI Design) 2nd Semester

w.e.f: 8th January, 2021

Time	9-10 AM	10-11 AM	11-12 AM	12-1 PM	1-2 PM	2-3 PM	3-4 PM	4-5 PM
Day								
Mon			DSCI	DSDV		DSDV	DVD	RE
Tue			DSCI	DSDV		DMC	DSDV Lab	
Wed	RE	RE	DSDV	DMC		DVD	DSDV Lab	
Thu			DSCI	DSDV		DVD	DVD Lab	
Fri			DSCI	DSDV		DMC	DMC Lab	

Sub. Code	Lab / Course Name	Faculty Name
MVD 102	Device Modeling & Circuit simulation Lab (DMC Lab)	Dr. Vandana Niranjana
MVD 104	Digital System Design with FPGA Lab (DSDV Lab)	Dr. Nidhi Goel
MVD 110	Lab based on elective(s) (DVD Lab)	Dr. Akash Tayal
MVD 122	Digital System Design with Verilog lab (DSDV Lab)	Dr. Shobha Sharma
MVD 102	Device Modeling & Circuit simulation (DMC)	Dr. Vandana Niranjana
MVD 104	Digital System Design with FPGA (DSD)	Dr. Nidhi Goel
MVD 110	Digital VLSI Design (DVD) (Elective)	Dr. Akash Tayal
MVD 106	Deep Submicron CMOS ICs (DSCI)	Dr. Jasdeep Kaur Dhanoa
MVD 122	Digital System Design with Verilog (DSDV)	Dr. Shobha Sharma
ROC 1 02	Research Ethics (RE)	Dr. Dinesh Ganotra

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F-AD-03


Even Sem. January –May 2021/ M. Tech (VLSI Design) 4thSemester

w.e.f: 8th January, 2021

Time	9-10 AM	10-11 AM	11-12 AM	12-1 PM	1-2 PM	2-3 PM	3-4 PM	4-5 PM
Day								
Mon			MVD-252 Dissertation -2/Project Work					
Tue								
Wed								
Thu								
Fri								

Sub. Code	Lab/Course Name
MVD252	Dissertation2/Project Work

Dr. Nidhi Goel
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